

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently amended)      A method comprising:  
  
        storing historical ~~hard-disk~~ performance data of a hard disk on a non-volatile memory unit of a system, the data including data identifying events that produced a spin-down of the hard disk and a period of time thereafter before the hard disk was spun up, the data being available on the memory unit after the system has been rebooted.
  
2. (Original)    The method of claim 1, wherein the non-volatile memory unit is a cache for the hard disk.
  
3. (Original)    The method of claim 2, wherein the non-volatile memory unit includes a form factor of a Mini Peripheral Component Interconnect Express card.

4. (Original) The method of claim 1, wherein the non-volatile memory unit includes a Peripheral Component Interconnect Express interface.
5. (Original) The method of claim 1, wherein the non-volatile memory unit consists of a thin film electronics memory.
6. (Currently amended) The method of claim 1, further including using the historical ~~hard-disk~~ performance data to implement a power management policy of the hard disk.
7. (Cancelled).
8. (Currently amended) A machine readable medium having stored thereon a set of instructions which when executed cause a system to perform a method comprising of:
- storing historical ~~hard-disk~~ performance data of a hard disk on a non-volatile memory unit of a system, the data including data identifying events that produced a spin-down of the hard disk and a period of time thereafter before the hard disk was spun up, the data being available on the memory unit after the system has been rebooted.

9. (Original) The machine readable medium of claim 8, wherein the non-volatile memory unit is a cache for the hard disk.

10. (Original) The machine readable medium of claim 9, wherein the non-volatile memory unit includes a form factor of a Mini Peripheral Component Interconnect Express card.

11. (Original) The machine readable medium of claim 8, wherein the non-volatile memory unit includes a Peripheral Component Interconnect Express interface.

12. (Original) The machine readable medium of claim 8, wherein the non-volatile memory unit consists of a thin film electronics memory.

13. (Cancelled).

14. (Currently amended) A system comprising of:

a processor;

a non-volatile cache coupled to the processor; and

a machine readable medium having stored thereon a set of instructions which  
when executed cause the system to perform a method comprising of:

storing historical ~~hard-disk~~ performance data of a hard disk on the non-volatile  
cache of a system, the data including data identifying events that produced a spin-down  
of the hard disk and a period of time thereafter before the hard disk was spun up, the data  
being available on the non-volatile cache after the system has been rebooted.

15. (Currently amended) The system of claim ~~[[ 8 ]]~~ 14, wherein the non-volatile  
cache is a cache for the hard disk.

16. (Original) The system of claim 14, wherein the non-volatile cache includes a form  
factor of a Mini Peripheral Component Interconnect Express card.

17. (Currently amended) The system of claim ~~[[ 8 ]]~~ 14, wherein the non-volatile  
cache includes a Peripheral Component Interconnect Express interface.

18. (Currently amended)      The system of claim ~~[[ 8 ]]~~ 14, wherein the non-volatile cache consists of a thin film electronics memory.

19. (Cancelled).